

TC 3864

U.S. PTO
09/703181

10/30/00	17	Subclass	ISSUE CLASSIFICATION
716		Class	

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PATENT NUMBER

U.S. UTILITY Patent Application

KW	O.I.P.E.	PATENT DATE
SCANNED	2003 04 07	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/703181		326 716	39 17	2019 3825	Liu KIK

APPLICANTS
Michael Moore
James LiuTITLE
Architecture for efficient implementation of serial data communication functions on a programmable logic device (PLD)PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
716	17	716	100		
INTERNATIONAL CLASSIFICATION		327	149		
G06F	7/50	326	38	39	
G06F	13/00				
G06F	7/38				
H03L	7/06				

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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/>	#	7	1	23	1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<i>ANDREA LIA</i> (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				Amount Due	Date Paid
ISSUE BATCH NUMBER					

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